

Two main features of the VBWV1 board are the operation of the internal and external E^2 memory. The internal E^2 memory is contained within the PIC MPU and used to store the set-up information for the S1D13705 display controller and various other settings of the board such as Baud rate etc. On power-up this information is used to configure the S1D13705 for a particular display (similar to a display BIOS) and set the various board features. This is very convenient as the S1D13705 does not retain its configuration on power-down. The external E^2 memory is used to store graphics images in bit-map or compressed RLE8 format. This enables images to be quickly displayed via simple commands from the host via the serial interface. This reduces overhead on the users host controller and overcomes the bandwidth

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problem of transferring large amounts of image data over the relatively slow serial bus.